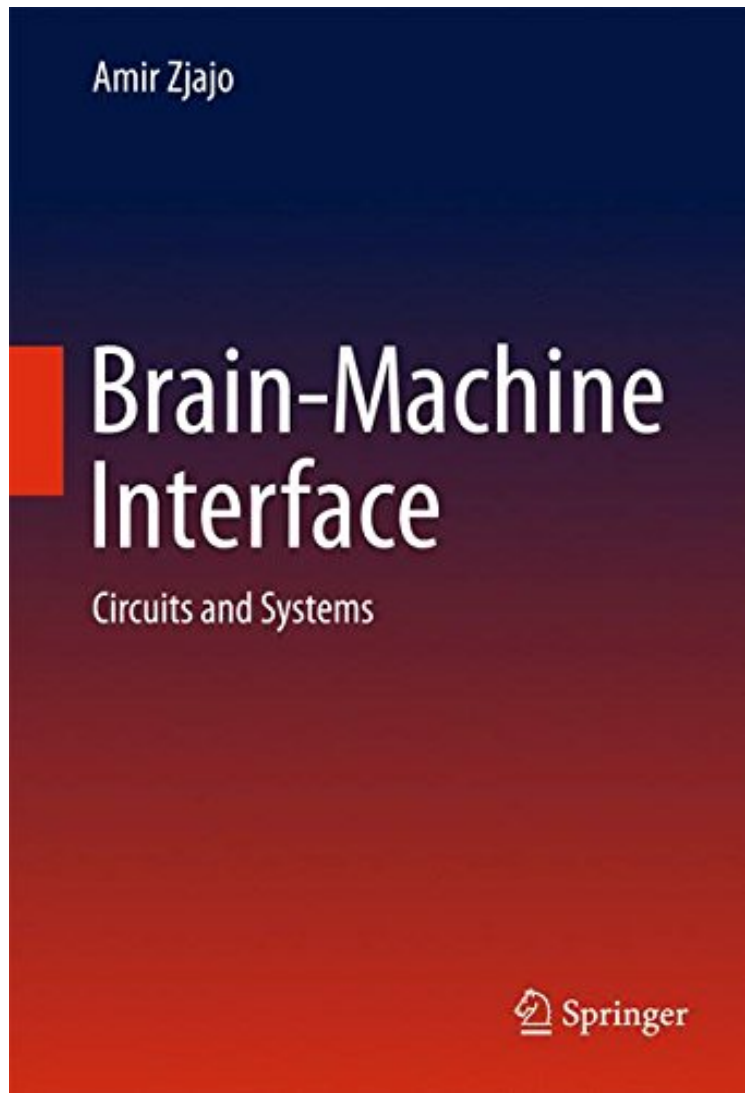


Brain-Machine Interface: Circuits and Systems

Amir Zjajo

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number of channels), feasible wireless data bandwidth and information quality to the delivered power of implantable system.

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About the Author Amir Zjajo received the M.Sc. and DIC degrees from the Imperial College London, London, U.K., in 2000 and the PhD. degree from Eindhoven University of Technology, Eindhoven, The Netherlands in 2010, all in electrical engineering. In 2000, he joined Philips Research Laboratories as a member of the research staff in the Mixed-Signal Circuits and Systems Group. From 2006 until 2009, he was with Corporate Research of NXP Semiconductors as a Senior Research Scientist. In 2009, he joined Delft University of Technology as a faculty member in Circuits and Systems group.

Dr. Zjajo has published more than 70 papers in referenced journals and conference proceedings, and holds more than 10 US patents or patent pending. He is the author of the books *Low-Voltage High-Resolution A/D Converters: Design, Test and Calibration* (Springer, 2011, Chinese translation, China Machine Press, 2015), and *Stochastic Process Variations in Deep-Submicron CMOS: Circuits and Algorithms* (Springer, 2014). He serves as a member of Technical Program Committee of IEEE Design, Automation and Test in Europe Conference, IEEE International Symposium on Circuits and Systems, IEEE International Symposium on VLSI, IEEE International Symposium on Nanoelectronic and Information Systems, and IEEE International Conference on Embedded Computer Systems. His research interests include power-efficient mixed-signal circuit and system design for health and mobile applications, and neuromorphic electronic circuits for autonomous cognitive systems. Dr. Zjajo won the best paper award at BIODEVICES 2015, and DATE 2012.